PTO/SB/08A (07-06)

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Substitute	e for form 1449A/PTO)		Complete if Known		
				Application Number	10/715,440	
INFO	RMATION	DISCI	LOSURE	Filing Date	November 19, 2003	
STAT	TEMENT BY	Y APP	LICANT	First Named Inventor	Monier MAHER	
				Art Unit	2109	
(Use as many sheets as necessary)				Examiner Name	Unknown	
Sheet	1	of	2	Attorney Docket Number	NVDA/AG-08-0114-US	

U.S. PATENT DOCUMENTS							
Examiner	Cite	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
Initials *	No.1	Number - Kind Code ² (if known)	MM-DD-YYYY	Document			
							
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	FOREIGN PATENT DOCUMENTS							
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Examiner Initials*	Cite No. ¹	Country Code³ - Number⁴ - Kind Code⁵ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Lines, Where Relevant Passages or Relevant Figures Appear	T [®]		
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Examiner Signature	/Kidest Bahta/	Date Considered	02/13/2009

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Translation is attached.

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Sheet	2	of	2	Attorney Docket Number	NVDA/AG-08-0114-US	

NON PATENT LITERATURE DOCUMENTS						
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
	A1	Bishop, et al. "SPARTA: Simulation of Physics on a Real-Time Architecture," Proceedings of the 10 th Great Lakes Symposium on VLSI, pp. 177-182, 2000.				
	A2	Intel Corp. "Intel PCI and PCI Express," 1992-2004.				
	А3	Telekinesys Research Ltd. "Havok Game Dynamics SDK," 2002.				

Examiner Signature	/Kidest Bahta/	Date Considered	02/13/2009

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